

## Inventor Information

Inventor One Given Name::	Alexander S.
Family Name::	Podkolzin
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Country of Residence::	Russia
Citizenship Country::	Russia

Inventor Two Given Name::	Valery B.
Family Name::	Kudryavtsev
Name Suffix::	
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Postal or Zip Code::	117571
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Country of Residence::	Russia
Citizenship Country::	Russia

## Correspondence Information

Name Line One::	Leo J. Peters
Address Line One::	LSI LOGIC CORPORATION
Address Line Two::	M/S D-106
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State or Province::	CA
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## Application Information

Title Line One::	METHOD AND APPARATUS FOR OPTIMIZING
Title Line Two::	THE TIMING OF INTEGRATED CIRCUITS
Total Drawing Sheets::	4
Formal Drawings?::	Yes
Application Type::	Utility
Docket Number::	L13.12-0141/00-684

## Representative Information

Registration Number One::	20,147
Registration Number Two::	24,383
Registration Number Three::	34,557
Registration Number Four::	34,797
Registration Number Five::	34,847
Registration Number Six::	35,612
Registration Number Seven::	36,188
Registration Number Eight::	38,354
Registration Number Nine::	39,650
Registration Number Ten::	39,758
Registration Number Eleven::	41,042
Registration Number Twelve::	41,885
Registration Number Thirteen::	42,413
Registration Number Fourteen::	44,401
Registration Number Fifteen::	33,807
Registration Number Sixteen::	43,331
Registration Number Seventeen::	36,771
Registration Number Eighteen::	33,279
Registration Number Nineteen::	33,562
Registration Number Twenty::	35,159